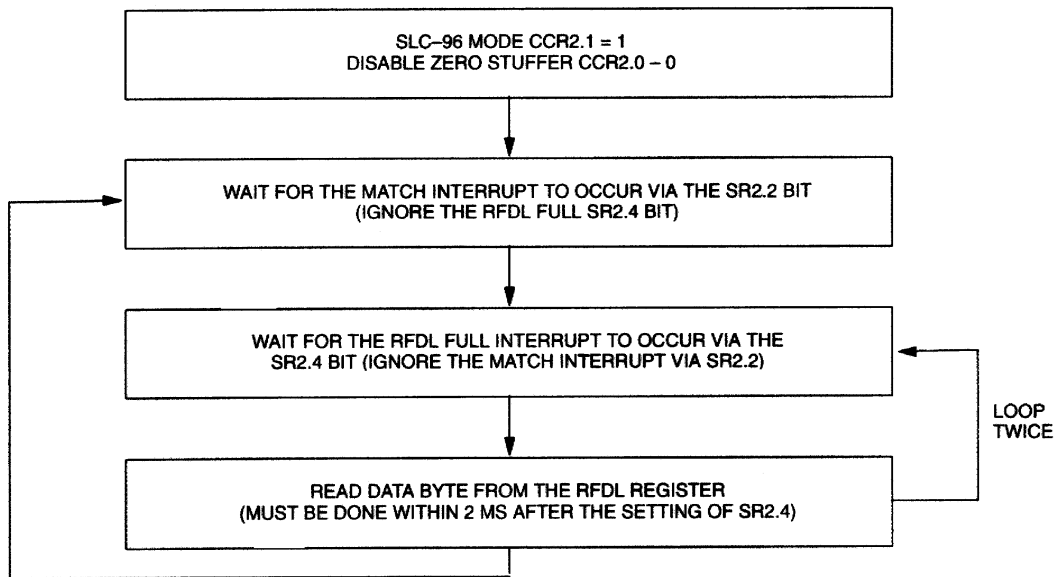




## SLC-96 MESSAGE FIELD EXTRACTION VIA RFDL Figure 1



### STATUS BIT(S) USED:

SR2.2 – RECEIVE FDL MATCH OCCURRENCE

SR2.4 – RECEIVE FDL BUFFER FULL

### REGISTER(S) USED:

RFDL – RECEIVE FDL REGISTER

## RFDL REGISTER BYTE SEQUENCE Figure 2

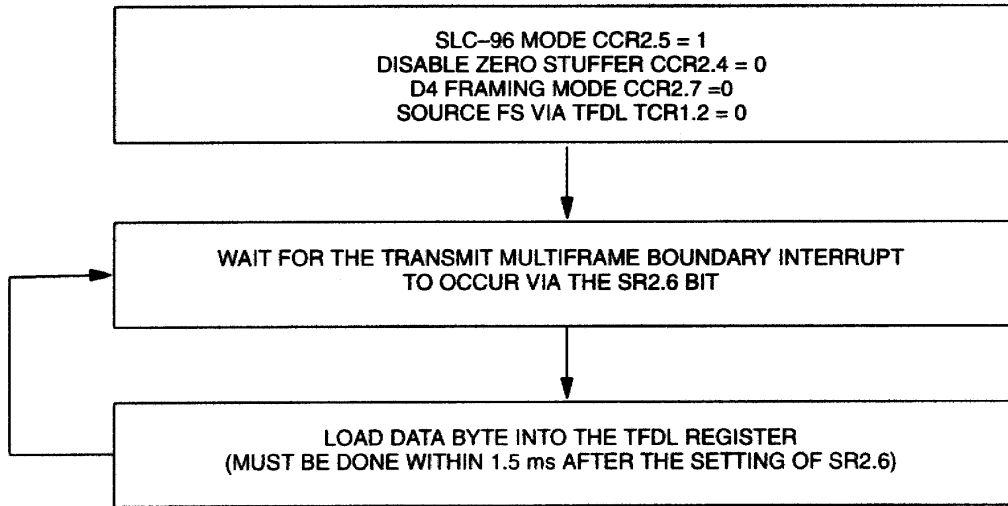
	(MSB)						(LSB)	
READ #1	C8	C7	C6	C5	C4	C3	C2	C1
READ #2	M2	M1	S=0	S=1	S=0	C11	C10	C9
READ #3	S=1	S4	S3	S2	S1	A2	A1	M3

## TRANSMIT SIDE SLC-96 APPLICATIONS

To insert the SLC-96 message fields, the user has the option to either use the external TLINK pin or to use the onboard TFDL register. Usage of the TLINK pin will require some external hardware and to enable this option, the TCR1.2 bit should be set to one. This Application Note concerns itself solely to the use of the TFDL register to insert the SLC-96 message fields.

Figure 3 displays the method to enable the DS2141A, DS2151 or DS2152 to insert the SLC-96 message fields via the TFDL register. On each normal D4 multi-frame boundary, the framer will signal to the user via the SR2.6 bit to write to the TFDL the sequence of bytes shown in Figure 4. The user will write to the TFDL six times in each SLC-96 multiframe.

## SLC-96 MESSAGE FIELD INSERTION VIA TFDL Figure 3



**STATUS BIT(S) USED:**

SR2.6 – TRANSMIT MULTIFRAME BOUNDARY

**REGISTER(S) USED:**

TFDL – TRANSMIT FDL REGISTER

## TFDL REGISTER BYTE SEQUENCE Figure 4

	(MSB)							(LSB)
Write #1	X	X	C1	1	1	1	0	0
Write #2	X	X	C7	C6	C5	C4	C3	C2
Write #3	X	X	S=1	S=0	C11	C10	C9	C8
Write #4	X	X	A2	A1	M3	M2	M1	S=0
Write #5	X	X	0	S=1	S4	S3	S2	S1
Write #6	X	X	0	1	1	1	0	0