DALLAS SEMICONDUCTOR

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NOTES:

1. Bt device is programmed for a "framed" input (i.e., Unframed Input control bit = 0).
2. RSYNC is programmed to output a frame sync; TSYNC is programmed to input a frame sync
3. Receive and transmit electric stores are disabled.
4. A "loop–timed" application is shown.
5. Timing is shown below: