ADSST-NAV-2100/2300 GLOBAL POSITIONING SYSTEM RECEIVER CHIPSET REFERENCE DESIGN

HIGHLIGHTS

- Single-Frequency Standard-Positioning Service (SPS)
- Twelve Parallel Channels
- DSP-Based Programmable Solution
- Computes User's Position, Velocity and Time
- Efficient Algorithms for Very Low Time to First Fix (TTFF), Even Without Almanac
- Soft Solution Approach for Correlation
- Easy Upgradability and Expandability

- TTL Output Through Serial Link
- Designed for Easy Customization
- Based on Either One or Two Fixed-Point Digital Signal Processors (DSPs) with On-Chip SRAM and Integrated I/O Peripherals Support
- Two Implementations with Four Form Factors Facilitate Design

- Spare Processing Power for Additional Features and Integrated Applications
- Carrier-Aided Tracking
- Supports 47 Geodetic Data
- NMEA-0183-Compatible Message Format for Host Communication
- Custom Binary Message Format for Host Communication
- Real-Time Executive-Based Software Architecture
- DGPS Compatibility



GPS Receiver Based on NAV-2100 Chipset



NAV-2100

Description

The NAV-2100 is a 12-channel Global Positioning System Receiver (GPSR) chipset reference design. It is designed around a programmable platform—two Analog Devices' ADSST-NAV-2100 fixed-point Digital Signal Processors with on-chip SRAM and integrated I/O peripherals support. The NAV-2100, together with a standard RF front end and GPS antenna, forms a complete GPS receiver.

The NAV-2100 chipset consists of the following:

- ADSST-NAV-2100
- Software License



Power Supply Voltage Board Dimensions

+5 V and +3.3 V 70 mm × 50 mm × 12 mm

NAV-2100 Evaluation Kit

The evaluation kit is an easy way to become familiar with the different features of the NAV-2100 chipset.

NAV-2100 GPS Evaluation Kit

1. NAV-2100 GPS receiver unit

- NAV-2100 GPS receiver board
- Motherboard
- Connectors and LED
- Metallic enclosure
- 2. Antenna with antenna cable
- 3. RS-232 cable and power cable
- GVISION, PC-based user interface software on a 3¹/₂" disk, provides information such as user position, velocity, heading, waypoint navigation in text and graphic form.
- User Guide Antenna with Antenna Cable

5. NAV-2100 User's Guide



<u>GPSR Chipset Reference Design</u>

Following are the different elements that comprise the NAV-2100 GPS Evaluation Kit.

NAV-2100 GPS Receiver Board: The NAV-2100 GPS receiver is the top board in the receiver unit. The receiver chipset comprises a footprint of 70 mm \times 50 mm \times 12 mm, which is smaller than a credit card. A dual inline male connector interfaces at TTL levels.

Motherboard: The motherboard is the bottom board of the NAV-2100 GPS Evaluation Kit. This board supplies the various power supply voltages required for the operation of the GPS receiver. It is tolerant to a wide range of input voltages ranging from 9 to 45 volts. It also provides a serial interface to the PC via a standard RS-232 driver.

Connectors and LED: The NAV-2100 GPS receiver unit has three connectors on its front panel. The power connector is a 3-pin male socket for 12-volt input from a standard car battery or any other source. The RS-232 connector is a bidirectional interface between the GPS receiver unit and any host system like the PC. In addition to the serial communication, the connector can also receive the RTCM corrections on a separate line. The antenna connector is an SMB-type receptacle to receive the satellite signals from the antenna. The LED indicates the availability of the power supply to the GPS receiver.

Metallic Enclosure: The enclosure ensures that no stray interference signal affects the performance of the GPS receiver board. The enclosure and the receiver board share a common ground.

Antenna with Antenna Cable: The antenna supplied is an active patch antenna with a magnetic base and built-in cable. The built-in feature decreases the number of joints in the signal path, minimizing loss. The cable is of adequate length (three meters) for ease of routing. The SMB end of the cable fits into the mating connector on the NAV-2100 GPS receiver unit.

RS-232 Cable and Power Cable: The RS-232 cable is a 9-pin cable with D-type connectors at both ends in a null modem configuration. In addition, a separate line is reserved for the RTCM data for DGPS.

The Power cable has a cigarette lighter connector at one end and a female 3-pin connector at the other end. Both cables are three meters long, which is considered adequate for most configurations. **GVISION**: This is PC-based user interface software, which enables the user to configure the receiver and monitor the messages to/from the receiver. GVISION provides information such as user position, speed, heading and other GPS parameters. It also provides a data logging command, which facilitates capturing data (binary and NMEA) output from the receiver into files for post processing and analysis. The graphic screen of GVISION provides a clear trace of the route covered by the receiver on field test. Waypoints can be set to aid in waypoint navigation.

NAV-2100 User's Guide: The User's Guide gives a complete description of the NAV-2100 GPS receiver operation. A Quick Start Guide provides the user with most elementary steps to be followed in order to use the receiver. A comprehensive description of GVISION is also included. The input/output message structure for all binary and NMEA messages will aid the user to transmit/ receive commands and data from software other than GVISION.



NAV-2K Family

NAV-2100A

Description

The NAV-2100A is a +3.3 V version, which includes a real-time clock and is packaged in a smaller form factor compatible with the Ashtech G8.



Power Supply Voltage Board Dimensions

+3.3 V 60 mm \times 40 mm \times 12 mm

NAV-2100R

Description

The NAV-2100R is a +5 V version, which includes a real-time clock and is packaged in a narrower form factor compatible with Conexant Jupiter and SiRF products.



Power Supply Voltage Board Dimensions

+5 V 71 mm \times 41 mm \times 12 mm



<u>GPSR Chipset Reference Design</u>

NAV-2300 Preliminary Information

Description

The NAV-2300 is a low power version, packaged in a smaller form factor, implemented with a single DSP. It is designed around a high performance programmable platform—the Analog Devices ADSST-NAV-2300 fixed-point Digital Signal Processor with onchip SRAM and integrated I/O peripherals support. The NAV-2300 together with a standard RF front end and GPS antenna forms a complete GPS receiver

The NAV-2300 chipset consists of the following:

- ADSST-NAV-2300
- Software License

The NAV-2300 reference design includes a real-time clock.



Power Supply Voltage Power Consumption Board Dimensions

+3.3 V <0.6 watt 52 mm × 42 mm × 12 mm

NAV-2300R Preliminary Information

Description

The NAV-2300R is a +5 V version, which includes a real-time clock and is packaged in a narrower form factor compatible with Conexant Jupiter and SiRF products.



Power Supply Voltage Power Consumption Board Dimensions +5 V <0.6 watt 71 mm × 41 mm × 12 mm

NAV-2K Family

Performance Characteristics

Receiver

12 channels L1-C/A code SPS

40 seconds (typical)

60 seconds (typical)

Warm Start (with Almanac, Position and Time Estimate) Cold Start (without Almanac, Time, Position) Hot Start (with Almanac, Position, Time and Ephemeris)

Time To First Fix (TTFF)

Position Accuracy

Horizontal Velocity

Dynamics

Velocity Acceleration

Jerk

Reacquisition Signal

Position

Satellite Data Collection

Position Solution

Position update rate

20 seconds (typical) 20 meters (1σ without S/A) 1–5 meters with DGPS 0.1 meters/second (1σ without S/A)

600 m/sec 4 *g* 7 m/sec³

Less than 1 second with a blockage time of up to 180 seconds 1 second 2 to 11 seconds for synchronization Continuous data collection and parity checking on all twelve channels 2D/3D position, velocity and time 47 geodetic datum supported (default WGS84) 1 second

Environmental Characteristics

Operational Temperature Range (Ambient) Storage Temperature Range Humidity

-45°C to +85°C -65°C to +150°C 95% noncondensing +30°C to +60°C -1000 to +18,000 meters

Altitude

PC/Host Communication

Interface Baud Rate Message Formats Serial TTL output 9600 baud NMEA0183 Ver 2.00 ASCII, as well as Accord's proprietary binary

Output Messages

Binary

User's present position in terms of Latitude, Longitude, Altitude, ECEF Coordinates, Speed, Heading, Time, DOP, Receiver Status, Satellite Data, Error Messages, Almanac

NMEA

\$GPGGA, \$GPGSA, \$GPRMC, \$GPGSV, \$GPGLL, \$GPZDA, \$GPVTG

Input Messages (Binary)

Force Satellite Reselection, Factory Reset, Almanac, Position, Time, Date and Geodetic Datum, Message Control and Configuration, DGPS Parameters, Receiver Startup Mode, DOP and Visibility Settings

Application Interface (Optional)

The Real Time Executive of the NAV-2100 GPS chipset provides a programmatic interface to integrate OEM application software

Analog Devices' NAV-2K GPS Family was developed in conjunction with Accord Software & Systems Pvt., Ltd.

EUROPE HEADQUARTERS

Am Westpark 1–3 D-81373 München, Germany Tel: 089/76 903-0; Fax: 089/76 903-157 JAPAN HEADQUARTERS New Pier Takeshiba, South Tower Building 1-16-1 Kaigan, Minato-ku, Tokyo 105-6891, Japan Tel: (3) 5402-8200; Fax: (3) 5402-1063 SOUTHEAST ASIA HEADQUARTERS 4501 Nat West Tower, Times Square Causeway Bay, Hong Kong, PRC Tel: (2) 506-9336; Fax: (2) 506-4755

WORLDWIDE HEADQUARTERS

One Technology Way, P.O. Box 9106 Norwood, MA 02062-9106, U.S.A. Tel: 781-461-3732 Fax: 781-461-4360 email: systems.solutions@analog.com World Wide Web Site: http://www.analog.com

