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# Dual High Output Current, High Speed Amplifier

## Preliminary Technical Data

## AD8017

### FEATURES

Low cost drive amplifiers provide 200mA, 10.2Vpp output. Ideal as a PC based, Customer Premise Equipment (CPE) driver in DSL applications.

Output Voltage 1.0V to the Rail into 100Ω

Low Cost

Low Power Operation

+5V to +12V Voltage Supply

7mA/amp Supply Current

Current Feedback Amplifiers

High Output Voltage and Current Drive

200mA Output Drive Current into 25Ω

500mA Short Circuit Output Drive Current

20Vp-p Differential Output Voltage,  $R_L = 50\Omega$

Low Distortion

-83dBc @ 500kHz SFDR,  $R_L = 100\Omega$ ,  $V_o =$

2Vp-p

1.9nV/ $\sqrt{\text{Hz}}$  Voltage noise density

High Speed

160MHz Bandwidth (-3dB)

1500V/ $\mu\text{S}$  Slew Rate

### PRODUCT DESCRIPTION

The AD8017 is a low cost, dual high speed amplifier capable of driving low distortion signals to within 1.0V of the supply rail. It is intended for use in single supply xDSL systems where low distortion and low cost is essential. The amplifiers will be able to drive minimum 200mA of output current per amplifier. The AD8017 will deliver -68dBc of SFDR at 500kHz, required for many xDSL applications.

Fabricated in ADI's high speed XFCB process, the high bandwidth and fast slew rate of the AD8017 keep distortion to a minimum, while dissipating a minimum of power. The quiescent current of the AD8017 is low 7mA/amp max.

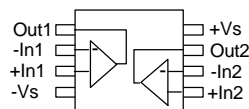
### APPLICATIONS

xDSL PCI Cards

Consumer DSL Modems

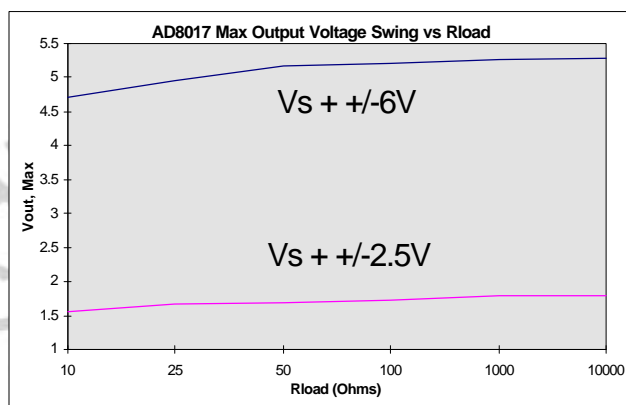
Line Driver

Video Distribution



AD8017

8 Pin 'Thermal Coastline' SOIC



Low distortion, high output voltage drive, and high output current drive make the AD8017 ideal for use in low cost

Customer Premise End (CPE) equipment for ADSL, SDSL, VDSL and proprietary xDSL systems.

The AD8017 drive capability comes in a very compact form. Utilizing ADI's proprietary 'Thermal Coastline' SOIC package, the AD8017's total (static and dynamic) power on +12V supplies is easily dissipated without external heat sink, other than to place the AD8017 on a 4-layer PCB.

The AD8017 will operate over the Commercial Temperature range 0°C to +85°C.

SPECIFICATIONS (@25°C,  $V_s=+/-6V$ ,  $R_L=100\Omega$ ,  $R_F=R_G=620\Omega$ , unless otherwise noted)

Parameter	Conditions	AD8017			Units
		Min	Typ	Max	
<b>DYNAMIC PERFORMANCE</b>					
-3dB Bandwidth	$G=+2$ , $V_{OUT}<0.4V$ p-p	TBD	160		MHz
0.1dB Bandwidth	$V_{OUT}<0.4V$ p-p		70		MHz
Large Signal Bandwidth	$V_{OUT}=4V$ p-p		TBD		MHz
Slew Rate	Non-Inverting, $V_{OUT}=2V$ p-p, $G=+2$	1500		V/ $\mu$ s	
Rise & Fall Time	Non-Inverting, $V_{OUT}=2V$ p-p		2.6		ns
Settling Time	0.1%, $V_{OUT}=2V$ p-p		14		ns
Peaking	$V_{OUT}=0.4V$ p-p, <5MHz		TBD		dB
<b>NOISE / HARMONIC PERFORMANCE</b>					
Distortion,	$V_{OUT}=2V$ p-p				
2 <sup>nd</sup> Harmonic	500kHz, $R_L=100\Omega$ /25 $\Omega$		-78/-71		dBc
	1MHz, $R_L=100\Omega$ /25 $\Omega$		-76/-69		dBc
3 <sup>rd</sup> Harmonic	500kHz, $R_L=100\Omega$ /25 $\Omega$		-105/-91		dBc
	1MHz, $R_L=100\Omega$ /25 $\Omega$		-81/-72		dBc
IP3	500kHz, $R_L=100\Omega$ /25 $\Omega$		TBD		dBm
IMD	500kHz, $R_L=100\Omega$ /25 $\Omega$		TBD		dBc
MTPR	26kHz to 1.1MHz		TBD		dBc
Input Noise Voltage	f=10kHz		1.9		nV $\sqrt$ Hz
Input Noise Current	f=10kHz (+ Inputs)		TBD		pA $\sqrt$ Hz
Input Noise Current	f=10kHz (- Inputs)		TBD		pA $\sqrt$ Hz
Crosstalk	f = 5MHz, $G=+2$		70		dB
<b>DC PERFORMANCE</b>					
Input Offset Voltage	Tmin-Tmax		1.5	TBD	mV
	$V_{OUT}=2V$ p-p			TBD	mV
Transimpedance Gain	Tmin-Tmax	TBD	800		K $\Omega$
		TBD			K $\Omega$
<b>INPUT CHARACTERISTICS</b>					
Input Resistance	+Input		TBD		K $\Omega$
	-Input		TBD		K $\Omega$
Input Capacitance	+Input		2		pF
Input Bias Current (-)	Tmin-Tmax		$\pm 10$	TBD	$\mu$ A
Input Bias Current (+)	Tmin-Tmax		$\pm 10$	TBD	$\mu$ A
CMRR	$V_{cm}=+/-2.5V$		60		dB
Input CM Voltage Range			$\pm 5.1$		V
<b>OUTPUT CHARACTERISTICS</b>					
Output Resistance			TBD		$\Omega$
Output Voltage Swing	$R_L=25\Omega$	TBD	$\pm 5.1$		V
Output Current	$R_L=25\Omega$	TBD	200		mA
Short-Circuit Current			500		mA
<b>POWER SUPPLY</b>					
Supply Current/Amp	Tmin - Tmax		7.0	TBD	mA
Operating Range	Dual Supply	$\pm$ TBD		$\pm 6.0$	V
Power Supply Rejection Ratio			66	TBD	dB
Operating Temperature Range		0		+85	Deg C

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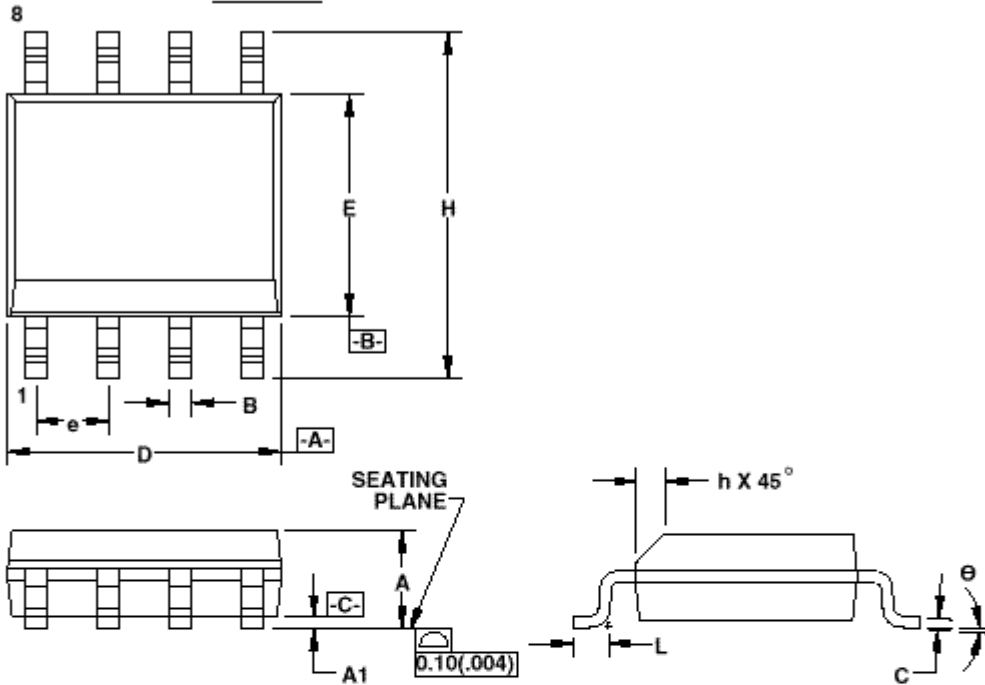
SPECIFICATIONS (@25°C,  $V_s = \pm 2.5V$ ,  $R_L = 100\Omega$ ,  $R_F = R_G = 620\Omega$ , unless otherwise noted)

Parameter	Conditions	AD8017			Units
		Min	Typ	Max	
<b>DYNAMIC PERFORMANCE</b>					
-3dB Bandwidth	$G = +2$ , $V_{OUT} < 0.4V$ p-p	TBD	120		MHz
0.1dB Bandwidth	$V_{OUT} < 0.4V$ p-p		TBD		MHz
Large Signal Bandwidth	$V_{OUT} = 4V$ p-p		TBD		MHz
Slew Rate	Non-Inverting, $V_{OUT} = 2V$ p-p, $G = +2$	TBD		$V/\mu s$	
Rise & Fall Time	Non-Inverting, $V_{OUT} = 2V$ p-p		1.3		ns
Settling Time	0.1%, $V_{OUT} = 2V$ p-p		14		ns
Peaking	$V_{OUT} = 0.4V$ p-p, $< 5MHz$		TBD		dB
<b>NOISE / HARMONIC PERFORMANCE</b>					
Distortion,	$V_{OUT} = 2V$ p-p				
2 <sup>nd</sup> Harmonic	500kHz, $R_L = 100\Omega / 25\Omega$		-75/-68		dBc
	1MHz, $R_L = 100\Omega / 25\Omega$		-73/-67		dBc
3 <sup>rd</sup> Harmonic	500kHz, $R_L = 100\Omega / 25\Omega$		-91/-90		dBc
	1MHz, $R_L = 100\Omega / 25\Omega$		-79/-79		dBc
IP3	500kHz, $R_L = 100\Omega / 25\Omega$		TBD		dBm
IMD	500kHz, $R_L = 100\Omega / 25\Omega$		TBD		dBc
MTPR	26kHz to 1.1MHz		TBD		dBc
Input Noise Voltage	$f = 10kHz$		1.8		$nV\sqrt{Hz}$
Input Noise Current	$f = 10kHz$ (+ Inputs)		18.9		$pA\sqrt{Hz}$
Input Noise Current	$f = 10kHz$ (- Inputs)		17.7		$pA\sqrt{Hz}$
Crosstalk	$f = 5MHz$ , $G = +2$		70		dB
<b>DC PERFORMANCE</b>					
Input Offset Voltage	$T_{min} - T_{max}$		1.5	TBD	mV
	$V_{OUT} = 2V$ p-p			TBD	mV
Transimpedance Gain	$T_{min} - T_{max}$	TBD	800		$K\Omega$
		TBD			$K\Omega$
<b>INPUT CHARACTERISTICS</b>					
Input Resistance	+Input		TBD		$K\Omega$
	-Input		TBD		$K\Omega$
Input Capacitance	+Input		2		pF
Input Bias Current (-)	$T_{min} - T_{max}$		$\pm 10$	TBD	$\mu A$
Input Bias Current (+)	$T_{min} - T_{max}$		$\pm 10$	TBD	$\mu A$
CMRR	$V_{cm} = \pm 1.0V$		60		dB
Input CM Voltage Range			$\pm 1.6$		V
<b>OUTPUT CHARACTERISTICS</b>					
Output Resistance			TBD		$\Omega$
Output Voltage Swing	$R_L = 25\Omega$	TBD	$\pm 1.6$		V
Output Current	$R_L = 25\Omega$	TBD	66		mA
Short-Circuit Current			TBD		mA
<b>POWER SUPPLY</b>					
Supply Current/Amp	$T_{min} - T_{max}$		6.6	TBD	mA
	Single Supply			TBD	mA
Operating Range		TBD		+5	V
Power Supply Rejection Ratio			63	TBD	dB
Operating Temperature Range		0		+85	Deg C

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Scale = 10x



MILLIMETERS		
Dimension	Min.	Max
A	1.35	1.75
A1	0.10	0.25
B	0.33	0.51
C	0.19	0.25
D	4.80	5.00
E	3.80	4.00
e	1.27 BSC.	
H	5.80	6.20
h	0.25	0.50
L	0.40	1.27
θ	0°	8°

INCHES		
Dimension	Min.	Max
A	.0532	.0688
A1	.0040	.0098
B	.013	.020
C	.0075	.0098
D	.1890	.1968
E	.1497	.1574
e	.050 BSC.	
H	.2284	.2440
h	.0099	.0196
L	.016	.050
θ	0°	8°

Title: 8L SOIC 150 mil  
Package Outline  
CUSTOMER

NOTES:

1. Controlling Dimensions are in mm.
2. All Dimensions per JEDEC Standards MS-012 AA

PRELIMINARY  
TECHNICAL  
DATA

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