

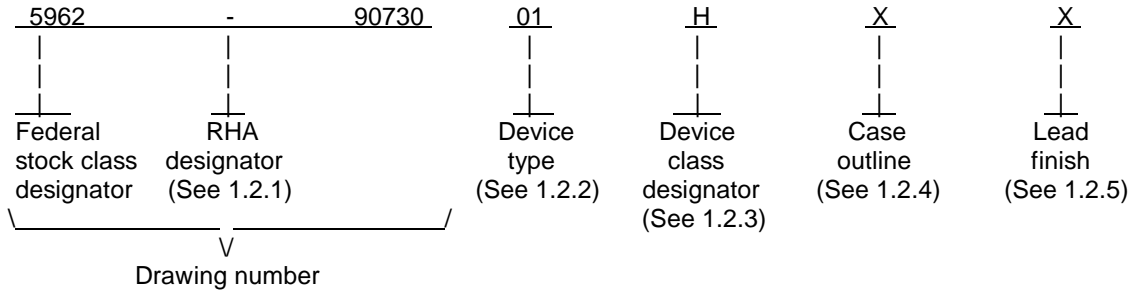
REVISIONS			
LTR	DESCRIPTION	DATE (YR-MO-DA)	APPROVED
A	Add vendor CAGE 34031. Add case outline Y. Editorial changes throughout.	92-01-06	Monica Poelking

REV																				
SHEET																				
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REV STATUS OF SHEETS		REV	A	A	A	A	A	A	A	A	A	A	A	A	A	A	A	A	A	
		SHEET	1	2	3	4	5	6	7	8	9	10	11	12	13					
PMIC N/A		PREPARED BY Robert M. Heber				DEFENSE ELECTRONICS SUPPLY CENTER DAYTON, OHIO 45444														
<b>STANDARDIZED MILITARY DRAWING</b>  THIS DRAWING IS AVAILABLE FOR USE BY ALL DEPARTMENTS AND AGENCIES OF THE DEPARTMENT OF DEFENSE  AMSC N/A		CHECKED BY Gary Zahn				MICROCIRCUIT, HIGH SPEED TRACK AND HOLD AMPLIFIER, HYBRID														
		APPROVED BY William K. Heckman																		
		DRAWING APPROVAL DATE 90-12-18				SIZE <b>A</b>	CAGE CODE <b>67268</b>	<b>5962-90730</b>												
		REVISION LEVEL A				SHEET 1 OF 13														

1. SCOPE

1.1 Scope. This drawing forms a part of a one part - one part number documentation system (see 6.6 herein). This drawing describes device requirements for hybrid microcircuits to be processed in accordance with MIL-H-38534. Two product assurance classes, military high reliability (device class H) and space application (device class K) and a choice of case outlines and lead finishes are available and are reflected in the Part or Identifying Number (PIN). When available, a choice of radiation hardness assurance levels are reflected in the PIN.

1.2 PIN. The PIN shall be as shown in the following example:



1.2.1 Radiation hardness assurance (RHA) designator. Device classes H and K RHA marked devices shall meet the MIL-H-38534 specified RHA levels and shall be marked with the appropriate RHA designator. A dash (-) indicates a non-RHA device.

1.2.2 Device type(s). The device type(s) shall identify the circuit function as follows:

<u>Device type</u>	<u>Generic number</u>	<u>Circuit function</u>
01	MN376, HCT-0300A	High speed track and hold amplifier

1.2.3 Device class designator. This device class designator shall be a single letter identifying the product assurance level as follows:

<u>Device class</u>	<u>Device requirements documentation</u>
H or K	Certification and qualification to MIL-H-38534

1.2.4 Case outline(s). The case outline(s) shall be as designated in appendix C of MIL-M-38510, and as follows:

<u>Outline letter</u>	<u>Case outline</u>
X	See figure 1 (24-lead, 1.315" x .810" x .170"), dual-in-line package.
Y	See figure 2 (24-lead, 1.280" x .780" x .175"), dual-in-line package.

1.2.5 Lead finish. The lead finish shall be as specified in MIL-H-38534 for classes H and K. Finish letter "X" shall not be marked on the microcircuit or its packaging. The "X" designation is for use in specifications when lead finishes A, B, and C are considered acceptable and interchangeable without preference.

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1.3 Absolute maximum ratings. 1/

Positive supply voltage ( $V_{CC}$ )-----	+18 V dc
Negative supply voltage ( $V_{EE}$ )-----	-18 V dc
Logic supply voltage ( $V_{DD}$ )-----	-0.5 V dc to +7 V dc
Analog input channels -----	$\pm V_{CC}$
Digital input-----	-0.5 V dc to +5.5 V dc
Power dissipation-----	1.025 W
Thermal resistance ( $\Theta_{JC}$ )-----	50° C/W
Thermal resistance ( $\Theta_{JA}$ )-----	70° C/W
Lead temperature (soldering, 10 seconds) -----	300° C
Storage temperature range-----	-65° C to +150° C
Junction temperature ( $T_J$ )-----	+175° C

1.4 Recommended operating conditions.

Positive supply voltage range ( $V_{CC}$ )-----	+14.55 V to +15.45 V dc
Negative supply voltage range ( $V_{EE}$ )-----	-14.55 V to -15.45 V dc
Logic supply voltage range ( $V_{DD}$ )-----	+4.75 V dc to +5.25 V dc
Case operating temperature range ( $T_C$ ) -----	-55° C to +125° C
Input voltage range-----	-10.0 V dc to +10.0 V dc
Output current -----	$\pm 20$ mA

2. APPLICABLE DOCUMENTS

2.1 Government specifications, standards, and handbook. Unless otherwise specified, the following specifications, standards, and handbook of the issue listed in that issue of the Department of Defense Index of Specifications and Standards specified in the solicitation, form a part of this drawing to the extent specified herein.

SPECIFICATIONS

MILITARY

- MIL-M-38510 - Microcircuits, General Specification for.
- MIL-H-38534 - Hybrid Microcircuits, General Specification for.

STANDARDS

MILITARY

- MIL-STD-480 - Configuration Control-Engineering Changes, Deviations and Waivers.
- MIL-STD-883 - Test Methods and Procedures for Microelectronics.

HANDBOOK

MILITARY

- MIL-HDBK-780 - Standardized Military Drawings.

(Copies of the specifications, standards, and handbook required by manufacturers in connection with specific acquisition functions should be obtained from the contracting activity or as directed by the contracting activity.)

1/ Stresses above the absolute maximum rating may cause permanent damage to the device. Extended operation at the maximum levels may degrade performance and affect reliability.

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TABLE I. Electrical performance characteristics.

Test	Symbol	Conditions 1/ -55° C ≤ T <sub>C</sub> ≤ +125° C unless otherwise specified	Device type	Group A subgroups	Limits		Unit
					Min	Max	
<b>ANALOG INPUTS</b>							
Input voltage range	V <sub>IN</sub>		01	1 2, 3 2/	-10.3 -10.3	+10.2 +10.2	V
Input resistance	R <sub>IN</sub>	V <sub>IN</sub> = +10 V 2/ V(pin 11) = 0 V V(pin 12) = 0 V T <sub>A</sub> = 25° C	01	4	0.75	2.00	kΩ
<b>DIGITAL INPUTS</b>							
Input voltage (high)	V <sub>IH</sub>	Logic "1"	01	1, 2, 3	+2.0		V
(low)	V <sub>IL</sub>	for all digital Logic "0" inputs				+0.8	
Input current (high)	I <sub>IH</sub>	V <sub>IH</sub> = +2.4 V	01	1, 2, 3		+1.0	mA
(low)	I <sub>IL</sub>	V <sub>IL</sub> = +0.4 V			-1.0		
<b>TRANSFER CHARACTERISTICS</b>							
Input offset voltage	V <sub>IO</sub>	Initial -55° C ≤ T <sub>C</sub> ≤ +125° C T <sub>C</sub> = +25° C End points T <sub>C</sub> = +25° C V(pin 13) = 0 V	01	2, 3 1 1	-35 -5.0 -12.5	+35 +5.0 +12.5	mV
Hold step (pedestal voltage)	V <sub>HS</sub>	T <sub>C</sub> = +25° C	01	4	-20	+20	mV
Pedestal voltage temperature sensitivity	V <sub>HS</sub> / T <sub>C</sub>		01	4, 5, 6	-80	+200	uV /°C
Gain error	AE	Initial -55° C ≤ T <sub>C</sub> ≤ +125° C T <sub>C</sub> = +25° C End points T <sub>C</sub> = +25° C	01	5, 6 4 4		±0.15 ±0.1 ±0.2	%
Gain linearity error	A <sub>L</sub>	Best straight line (5 points)	01	4, 5, 6		±0.01	%FSR
<b>ANALOG OUTPUTS</b>							
Output resistance	R <sub>O</sub>		01	1, 2, 3		1.0	Ω

See footnotes at end of table.

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TABLE I. Electrical performance characteristics - Continued.

Test	Symbol	Conditions <u>1/</u> -55°C ≤ T <sub>C</sub> ≤ +125°C unless otherwise specified	Device type	Group A subgroups	Limits		Unit	
					Min	Max		
<b>DYNAMIC CHARACTERISTICS</b>								
Hold mode droop	V <sub>HD</sub>	Initial -55°C ≤ T <sub>C</sub> ≤ +25°C	01	4, 6	-5.0	+5.0	uV/us	
		<u>T<sub>C</sub> = +125°C</u>			5	-1.8	+3.0	mV/us
		End points T <sub>C</sub> = +25°C			4	-10		
Track-to-hold transient voltage	V <sub>TTHT</sub>	<u>2/</u>	01	9,10,11		380	mVp-p	
Acquisition time	t <sub>a</sub>	10 V step to ±1 mV <u>2/</u>	01	9,10,11		200	ns	
		10 V step to ±10 mV <u>2/</u>				170		
		1 V step to ±10 mV <u>2/</u>				100		
Transient response (settling time, track-to-hold)	t <sub>r</sub> (t <sub>s</sub> )	Settling to ±1 mV <u>2/</u>	01	9,10,11		100	ns	
		Settling to ±10 mV <u>2/</u>				85		
Feedthrough rejection ratio	FRR	V <sub>IN</sub> = 20 Vp-p at 2.5 MHz T <sub>C</sub> = +25°C	01	4	64		dB	
Slew rate	SR	V <sub>IN</sub> = -5 V to +5 V step, <u>2/</u> V(pin 11) = 0 V, V(pin 12) = 0 V, T <sub>C</sub> = +25°C	01	4	120		V/us	
Bandwidth, small signal (-3 dB)	BW	V <sub>IN</sub> = 1 Vp-p, <u>2/</u> V(pin 11) = 0 V, V(pin 12) = 0 V, T <sub>C</sub> = +25°C	01	4	8		MHz	
Aperature time	t <sub>ap</sub>	<u>2/</u>	01	9,10,11		16	ns	
Aperature jitter	j <sub>ap</sub>	<u>2/</u> T <sub>C</sub> = +25°C	01	4	-50	+50	ps	

See footnotes at end of table.

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TABLE I. Electrical performance characteristics - Continued.

Test	Symbol	Conditions <sup>1/</sup> -55°C ≤ T <sub>C</sub> ≤ +125°C unless otherwise specified	Device type	Group A subgroups	Limits		Unit
					Min	Max	
<b>POWER SUPPLY</b>							
Supply current: pos supply (V <sub>CC</sub> )	I <sub>CC</sub>		01	1, 2, 3		+30	mA
neg supply (V <sub>EE</sub> )	I <sub>EE</sub>					-30	
logic supply (V <sub>DD</sub> )	I <sub>DD</sub>					+25	
Power consumption	P <sub>D</sub>	T <sub>C</sub> = +25°C	01	1		1025	mW
Power supply rejection ratio: pos supply (V <sub>CC</sub> )	PSSR1		01	1, 2, 3	-5	+5	mV/V
neg supply (V <sub>EE</sub> )	PSSR2				-5	+5	
logic supply (V <sub>DD</sub> )	PSSR3				-5	+5	

<sup>1/</sup> V<sub>CC</sub> = +15 V, V<sub>EE</sub> = -15 V, V<sub>DD</sub> = 5 V unless otherwise specified.

<sup>2/</sup> Parameter shall be tested as part of device initial characterization and after design and process changes. Parameter shall be guaranteed to the limits specified in table 1 for all lots not specifically tested.

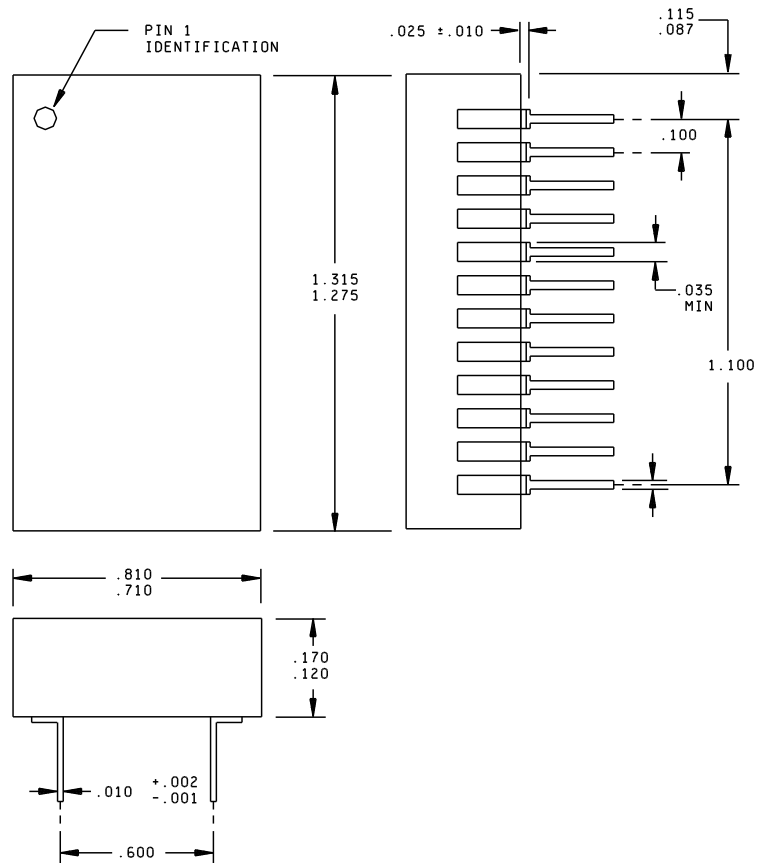
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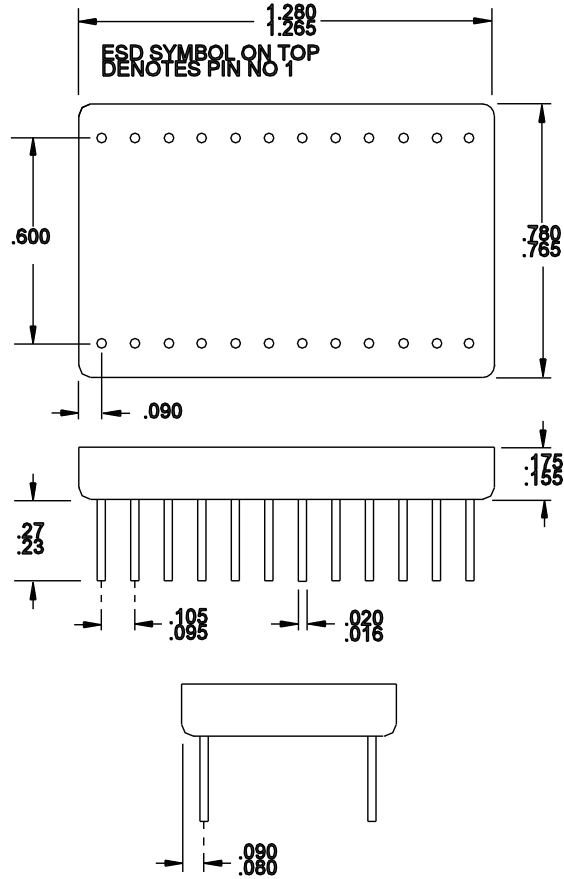
Inches	mm	Inches	mm	Inches	mm	Inches	mm
.001	0.03	.035	0.89	.120	3.05	.810	20.57
.002	0.05	.087	2.21	.170	4.32	1.100	27.94
.010	0.25	.100	2.54	.600	15.24	1.275	32.38
.025	0.64	.115	2.92	.710	18.03	1.315	33.40

NOTES:

1. Dimensions are in inches.
2. Metric equivalents are given for general information only.
3. Unless otherwise specified, tolerances are  $\pm 0.005$  (0.13 mm).

FIGURE 1. Case outline X.

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NOTES:

1. Dimensions are in inches.
2. Metric equivalents are given for general information only.
3. Unless otherwise specified, tolerances are  $\pm 0.005$  (0.13 mm).

FIGURE 2. Case outline Y.

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Device Type 01  
Case outlines X and Y

Pin	Function	Pin	Function
1	Analog output	13	Analog input
2	N/C	14	N/C
3	N/C	15	Input ground
4	N/C	16	N/C
5	N/C	17	N/C
6	N/C	18	N/C
7	N/C	19	N/C
8	N/C	20	N/C
9	V <sub>DD</sub>	21	Ground
10	Ground	22	V <sub>EE</sub>
11	Hold	23	Ground
12	Hold	24	V <sub>CC</sub>

FIGURE 3. Terminal connections.

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2.2 Order of precedence. In the event of a conflict between the text of this drawing and the references cited herein, the text of this drawing shall take precedence.

### 3. REQUIREMENTS

3.1 Item requirements. The individual item requirements shall be in accordance with MIL-H-38534 and as specified herein.

3.2 Design, construction, and physical dimensions. The design, construction, and physical dimensions shall be as specified in MIL-H-38534 and herein.

3.2.1 Case outline(s). The case outline(s) shall be in accordance with 1.2.4 herein and figure 1 and 2.

3.2.2 Terminal connections. The terminal connections shall be as specified on figure 3.

3.3 Electrical performance characteristics. Unless otherwise specified herein, the electrical performance characteristics are as specified in table I and shall apply over the full specified operating temperature range.

3.4 Electrical test requirements. The electrical test requirements shall be the subgroups specified in table II. The electrical tests for each subgroup are described in table I.

3.5 Marking. Marking shall be in accordance with MIL-H-38534. The part shall be marked with the PIN listed in 1.2 herein. In addition, the manufacturer's PIN may also be marked as listed in QML-38534.

3.6 Manufacturer eligibility. In addition to the general requirements of MIL-H-38534, the manufacturer of the part described herein shall submit for DESC-ECT review and approval electrical test data (variables format) on 22 devices from the initial quality conformance inspection group A lot sample, produced on the certified line, for each device type listed herein. The data should also include a summary of all parameters manually tested, and for those which, if any, are guaranteed.

3.7 Certificate of compliance. A certificate of compliance shall be required from a manufacturer in order to supply to this drawing. The certificate of compliance submitted to DESC-ECT shall affirm that the manufacturer's product meets the requirements of MIL-H-38534 and the requirements herein.

3.8 Certificate of conformance. A certificate of conformance as required in MIL-H-38534 shall be provided with each lot of microcircuits delivered to this drawing.

### 4. QUALITY ASSURANCE PROVISIONS

4.1 Sampling and inspection. Sampling and inspection procedures shall be in accordance with MIL-H-38534.

4.2 Screening. Screening shall be in accordance with MIL-H-38534. The following additional criteria shall apply:

a. Burn-in test, method 1015 of MIL-STD-883.

(1) Test condition A, B, C or D using the circuit submitted with the certificate of compliance (see 3.7 herein).

(2)  $T_A$  as specified in accordance with table I of method 1015 of MIL-STD-883.

b. Interim and final electrical test parameters shall be as specified in table II herein, except interim electrical parameter tests prior to burn-in are optional at the discretion of the manufacturer.

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TABLE II. Electrical test requirements.

MIL-STD-883 test requirements	Subgroups (per method 5008, group A test table)
Interim electrical parameters	- - -
Final electrical test parameters	1*,2,3,4,5,6, 9,10,11
Group A test requirements	1,2,3,4,5,6, 9,10,11
Group C end-point electrical parameters	1
MIL-STD-883 test requirements	Subgroups (per method 5008, table X)
Group E end-point electrical parameters	

\*PDA applies to subgroup 1.

4.3 Quality conformance inspection. Quality conformance inspection shall be in accordance with MIL-H-38534 and as specified herein.

4.3.1 Group A inspection. Group A inspection shall be in accordance with MIL-H-38534 and as follows:

- a. Tests shall be as specified in table II herein.
- b. Subgroups 7 and 8 shall be omitted.

4.3.2 Group B inspection. Group B inspection shall be in accordance with MIL-H-38534.

4.3.3 Group C inspection. Group C inspection shall be in accordance with MIL-H-38534 and as follows:

- a. End-point electrical parameters shall be as specified in table II herein.
- b. Steady-state life test conditions, method 1005 of MIL-STD-883.
  - (1) Test condition A, B, C or D using the circuit submitted with the certificate of compliance (see 3.7 herein).
  - (2)  $T_A$  as specified in accordance with table I of method 1005 of MIL-STD-883.
  - (3) Test duration: 1,000 hours, except as permitted by method 1005 of MIL-STD-883.

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4.3.4 Group D inspection. Group D inspection shall be in accordance with MIL-H-38534.

4.3.5 Group E inspection. Group E inspection is required only for parts intended to be marked as radiation hardness assured (see 3.5 herein). RHA levels for device classes H and K shall be M, D, R, and H. RHA quality conformance inspection sample tests shall be performed at the RHA level specified in the acquisition document.

- a. RHA tests for device classes H and K for levels M, D, R, and H shall be performed through each level to determine at what levels the devices meet the RHA requirements. These RHA tests shall be performed for initial qualification and after design or process changes which may affect the RHA performance of the device.
- b. End-point electrical parameters shall be as specified in table II herein.
- c. Prior to total dose irradiation, each selected sample shall be assembled in its qualified package. It shall pass the specified group A electrical parameters in table I for subgroups specified in table II herein.
- d. For device classes H and K, the devices shall be subjected to radiation hardness assured tests as specified in MIL-H-38534 for RHA level being tested, and meet the postirradiation end-point electrical parameter limits as defined in table I at  $T_A = +25^\circ\text{C} \pm 5$  percent, after exposure.
- e. Prior to and during total dose irradiation testing, the devices shall be biased to establish a worst case condition as specified in the radiation exposure circuit.
- f. For device classes H and K, subgroups 1 and 2 in table V, method 5005 of MIL-STD-883 shall be tested as appropriate for device construction.
- g. When specified in the purchase order or contract, a copy of the RHA delta limits shall be supplied.

5. PACKAGING

5.1 Packaging requirements. The requirements for packaging shall be in accordance with MIL-H-38534.

6. NOTES

6.1 Intended use. Microcircuits conforming to this drawing are intended for use for Government microcircuit applications (original equipment), design applications, and logistics purposes.

6.2 Replaceability. Microcircuits covered by this drawing will replace the same generic device covered by a contractor-prepared specification or drawing.

6.3 Configuration control of SMD's. All proposed changes to existing SMD's will be coordinated with the users of record for the individual documents. This coordination will be accomplished in accordance with MIL-STD-481 using DD Form 1693, Engineering Change Proposal (Short Form).

6.4 Record of users. Military and industrial users shall inform Defense Electronics Supply Center when a system application requires configuration control and the applicable SMD. DESC will maintain a record of users and this list will be used for coordination and distribution of changes to the drawings. Users of drawings covering microelectronic devices (FSC 5962) should contact DESC-ECT, telephone (513) 296-6047.

6.5 Comments. Comments on this drawing should be directed to DESC-ECT, Dayton, Ohio 45444, or telephone (513) 296-5374.

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6.6 One part - one part number system. The one part - one part number system described below has been developed to allow for transitions between identical generic devices covered by the four major microcircuit requirements documents (MIL-M-38510, MIL-H-38534, MIL-I-38535, and 1.2.1 of MIL-STD-883) without the necessity for the generation of unique PIN's. The four military requirements documents represent different class levels, and previously when a device manufacturer upgraded military product from one class level to another, the benefits of the upgraded product were unavailable to the Original Equipment Manufacturer (OEM), that was contractually locked into the original unique PIN. By establishing a one part number system covering all four documents, the OEM can acquire to the highest class level available for a given generic device to meet system needs without modifying the original contract parts selection criteria.

<u>Military documentation format</u>	<u>Example PIN under new system</u>	<u>Manufacturing source listing</u>	<u>Document listing</u>
New MIL-M-38510 Military Detail Specifications (in the SMD format)	5962-XXXXXZZ(B or S)YY (Part 1 or 2)	QPL-38510	MIL-BUL-103
New MIL-H-38534 Standardized Military Drawings	5962-XXXXXZZ(H or K)YY	QML-38534	MIL-BUL-103
New MIL-I-38535 Standardized Military Drawings	5962-XXXXXZZ(Q or V)YY	QML-38535	MIL-BUL-103
New 1.2.1 of MIL-STD-883 Standardized Military Drawings	5962-XXXXXZZ(M)YY	MIL-BUL-103	MIL-BUL-103

6.7 Sources of supply for device classes H and K. Sources of supply for device classes H and K are listed in QML-38534. The vendors listed in QML-38534 have submitted a certificate of compliance (see 3.7 herein) to DESC-ECT and have agreed to this drawing.

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STANDARDIZED MILITARY DRAWING SOURCE APPROVAL BULLETIN

DATE: 92-01-06

Approved sources of supply for SMD 5962-90730 are listed below for immediate acquisition only and shall be added to QML-38534 during the next revision. QML-38534 will be revised to include the addition or deletion of sources. The vendors listed below have agreed to this drawing and a certificate of compliance has been submitted to and accepted by DESC-ECT. This bulletin is superseded by the next dated revision of QML-38534.

Standardized military drawing PIN	Vendor CAGE number	Vendor similar PIN <sup>1/</sup>
5962-9073001XX	50507	MN376H/B
5962-9073001YX	34031	HTC-0300AM/883B

<sup>1/</sup> Caution. Do not use this number for item acquisition. Items acquired to this number may not satisfy the performance requirements of this drawing.

Vendor CAGE number

Vendor name and address

50507

Unitrode, Micro Networks Division  
324 Clark Street  
Worcester, MA 01606

34031

Analog Devices Computer Labs  
7910 Triad Center Drive  
Greensboro, NC 27409-9605

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