

## Preliminary Technical Data

## ADP3338

### FEATURES

High Accuracy Over Line and Load:  $\pm 0.9\%$  @  $+25^\circ\text{C}$ ,  
 $\pm 1.5\%$  Over Temperature

Ultralow Dropout Voltage: 700 mV (Typ) @ 1 A

Requires Only  $C_o = 1.0 \mu\text{F}$  for Stability

anyCAP = Stable with Any Type of Capacitor  
(Including MLCC)

Current and Thermal Limiting

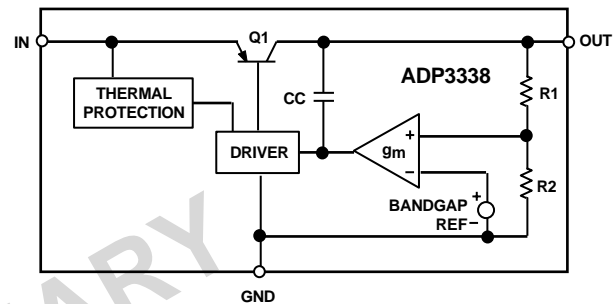
Low Noise

+2.6 V to +12 V Supply Range

$-40^\circ\text{C}$  to  $+85^\circ\text{C}$  Ambient Temperature Range

SOT-223 Package

### FUNCTIONAL BLOCK DIAGRAM



### APPLICATIONS

Notebook, Palmtop Computers

SCSI Terminators

Battery Powered Systems

PCMCIA Regulator

Bar Code Scanners

Camcorders, Cameras

### GENERAL DESCRIPTION

The ADP3338 is a member of the ADP330x family of precision low dropout anyCAP voltage regulators. The ADP3338 operates with an input voltage range of +2.6 V to +12 V and delivers a load current up to 1 A. The ADP3338 stands out from the conventional LDOs with a novel architecture and an enhanced process that enables it to offer performance advantages and higher output current than its competition. Its patented design requires only a  $1.0 \mu\text{F}$  output capacitor for stability. This device is insensitive to output capacitor Equivalent Series Resistance (ESR), and is stable with any good quality capacitor, including ceramic (MLCC) types for space-restricted applications. The ADP3338 achieves exceptional accuracy of  $\pm 0.9\%$  at room temperature and  $\pm 1.5\%$  over temperature, line and load variations. The dropout voltage of the ADP3338 is only 700 mV (typical) at 1 A. This device also includes a safety current limit, thermal overload protection and a shutdown feature. In shutdown mode, the ground current is reduced to less than  $2 \mu\text{A}$ . The ADP3338 has ultralow quiescent current  $70 \mu\text{A}$  (typ) in light load situations.

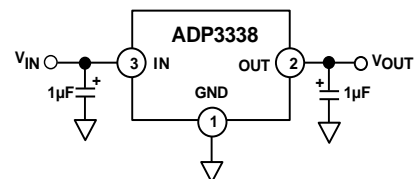


Figure 1. Typical Application Circuit

REV. PrB 1/6/00

Information furnished by Analog Devices is believed to be accurate and reliable. However, no responsibility is assumed by Analog Devices for its use, nor for any infringements of patents or other rights of third parties which may result from its use. No license is granted by implication or otherwise under any patent or patent rights of Analog Devices.

# ADP3338—SPECIFICATIONS<sup>1,2</sup>

( $V_{IN} = 7.0\text{ V}$ ,  $C_{IN} = C_{OUT} = 1.0\mu\text{F}$ ,  $T_A = -40^\circ\text{C}$  to  $+85^\circ\text{C}$ , unless otherwise noted)

Parameter	Symbol	Conditions	Min	Typ	Max	Units
OUTPUT						
Voltage Accuracy <sup>3</sup>	$V_{OUT}$	$V_{IN} = V_{OUTNOM} + 0.8\text{ V}$ to $+12\text{ V}$ $I_L = 0.1\text{ mA}$ to $1\text{ A}$ $T_A = +25^\circ\text{C}$	-0.9		+0.9	%
		$V_{IN} = V_{OUTNOM} + 0.8\text{ V}$ to $+12\text{ V}$ $I_L = 0.1\text{ mA}$ to $1\text{ A}$ $T_A = -20^\circ\text{C}$ to $85^\circ\text{C}$	-1.5		+1.5	%
		$V_{IN} = V_{OUTNOM} + 0.8\text{ V}$ to $+12\text{ V}$ $I_L = 0.1\text{ mA}$ to $750\text{ mA}$ $T_A = -40^\circ\text{C}$ to $85^\circ\text{C}$	-1.5		+1.5	%
Line Regulation <sup>3</sup>		$V_{IN} = V_{OUTNOM} + 0.8\text{ V}$ to $+12\text{ V}$ $T_A = +25^\circ\text{C}$		0.04		mV/V
Load Regulation		$I_L = 0.1\text{ mA}$ to $1\text{ A}$ $T_A = +25^\circ\text{C}$		0.04		mV/mA
Dropout Voltage	$V_{DROP}$	$V_{OUT} = 98\%$ of $V_{OUTNOM}$ $I_L = 1\text{ A}$		500	700	mV
		$I_L = 500\text{ mA}$		250	350	mV
		$I_L = 100\text{ mA}$		120	150	mV
		$I_L = 0.1\text{ mA}$		80	130	mV
Peak Load Current	$I_{LDPK}$	$V_{IN} = V_{OUTNOM} + 1\text{ V}$		1.4		A
Output Noise	$V_{NOISE}$	$f = 10\text{ Hz}-100\text{ kHz}$ , $C_L = 10\mu\text{F}$ $I_L = 1\text{ A}$ , $C_{NR} = 10\text{ nF}$		47		$\mu\text{V rms}$
		$f = 10\text{ Hz}-100\text{ kHz}$ , $C_L = 10\mu\text{F}$ $I_L = 1\text{ A}$ , $C_{NR} = 0\text{ nF}$		95		$\mu\text{V rms}$
GROUND CURRENT						
In Regulation	$I_{GND}$	$I_L = 1\text{ A}$		10	50	mA
		$I_L = 500\text{ mA}$		5	25	mA
		$I_L = 100\text{ mA}$		1	5	mA
		$I_L = 0.1\text{ mA}$		70	130	$\mu\text{A}$
In Dropout	$I_{GND}$	$V_{IN} = V_{OUTNOM} - 100\text{ mV}$ $I_L = 0.1\text{ mA}$		80	150	$\mu\text{A}$
In Shutdown	$I_{GNDS}$	$\overline{SD} = 0\text{ V}$ , $V_{IN} = 12\text{ V}$		0.01	2	$\mu\text{A}$
SHUTDOWN						
Threshold Voltage	$V_{\overline{THSD}}$	ON	2.0			V
		OFF			0.4	V
$\overline{SD}$ Input Current	$I_{\overline{SD}}$	$0 \leq \overline{SD} \leq 5\text{ V}$		1.4	6	$\mu\text{A}$
Output Current In Shutdown	$I_{\overline{OSD}}$	$T_A = +25^\circ\text{C}$ $V_{IN} = 12\text{ V}$			1	$\mu\text{A}$
		$T_A = +85^\circ\text{C}$ $V_{IN} = 12\text{ V}$			2	$\mu\text{A}$

## NOTES

1 Ambient temperature of  $+85^\circ\text{C}$  corresponds to a junction temperature of  $+125^\circ\text{C}$  under typical full load test conditions.

2 Application stable with no load.

3  $V_{IN} = 2.6\text{ V}$  for models with  $V_{OUTNOM} \leq 1.85\text{ V}$ .

Specifications subject to change without notice.

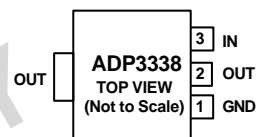
**ABSOLUTE MAXIMUM RATINGS\***

Input Supply Voltage .....	-0.3 V to +16 V
Power Dissipation .....	Internally Limited
Operating Ambient Temperature Range .....	-40°C to +85°C
Operating Junction Temperature Range .....	-40°C to +125°C
$\theta_{JA}$ .....	+62.3°C/W
$\theta_{JC}$ .....	TBD
Storage Temperature Range .....	-65°C to +150°C
Lead Temperature Range (Soldering 10 sec) .....	+300°C
Vapor Phase (60 sec) .....	+215°C
Infrared (15 sec) .....	+220°C

\*This is a stress rating only; operation beyond these limits can cause the device to be permanently damaged.

**PIN DESCRIPTION**

Pin	Name	Function
1	GND	Ground Pin.
2	OUT	Output of the Regulator. Bypass to ground with a 1.0 $\mu$ F or larger capacitor.
3	IN	Regulator Input.

**PIN CONFIGURATION****ORDERING GUIDE**

Model	Output Voltage*	Package Option	Marking Code
ADP3338ARM-1.8	1.8 V	RT (SOT-223)	LGA
ADP3338ARM-2.5	2.5 V	RT (SOT-223)	LGC
ADP3338ARM-2.85	2.85 V	RT (SOT-223)	LGD
ADP3338ARM-3.3	3.3 V	RT (SOT-223)	LGE
ADP3338ARM-5	5 V	RT (SOT-223)	LGF

\*Contact the factory for other output voltage options.

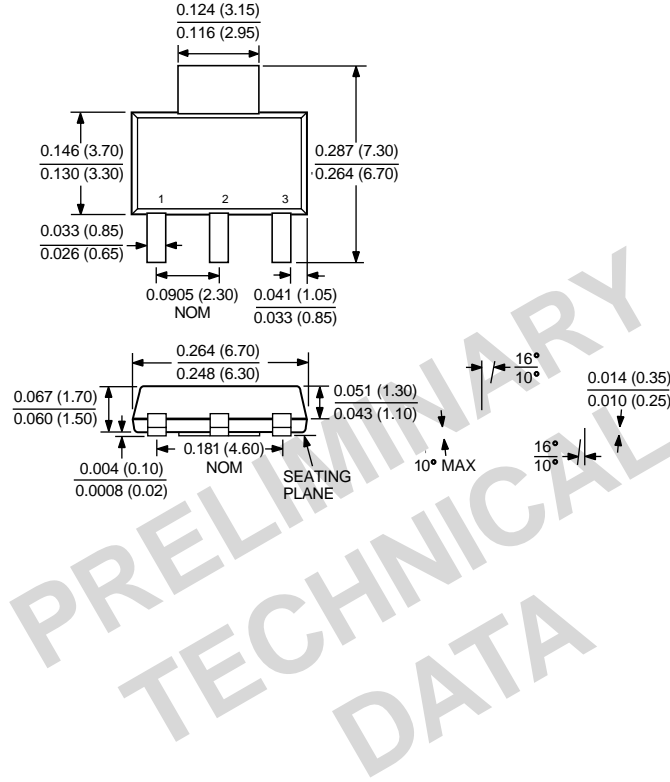
**CAUTION**

ESD (electrostatic discharge) sensitive device. Electrostatic charges as high as 4000 V readily accumulate on the human body and test equipment and can discharge without detection. Although this device features proprietary ESD protection circuitry, permanent damage may occur on devices subjected to high energy electrostatic discharges. Therefore, proper ESD precautions are recommended to avoid performance degradation or loss of functionality.



**OUTLINE DIMENSIONS**  
Dimensions shown in inches and (mm).

3-Lead Surface Mount  
RT-3 (SOT-223)



6/18/99

PRINTED IN U.S.A.